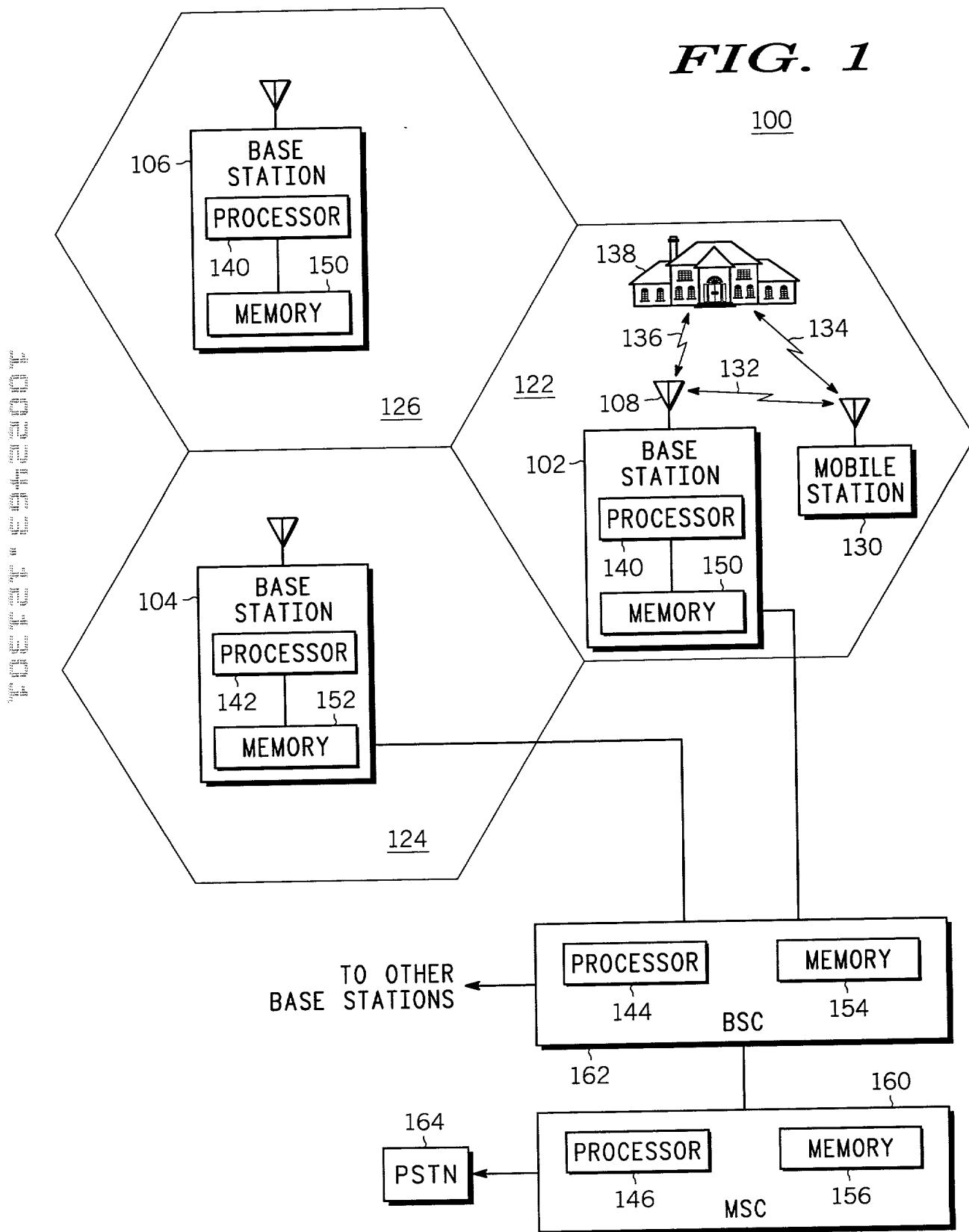


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FIG. 1



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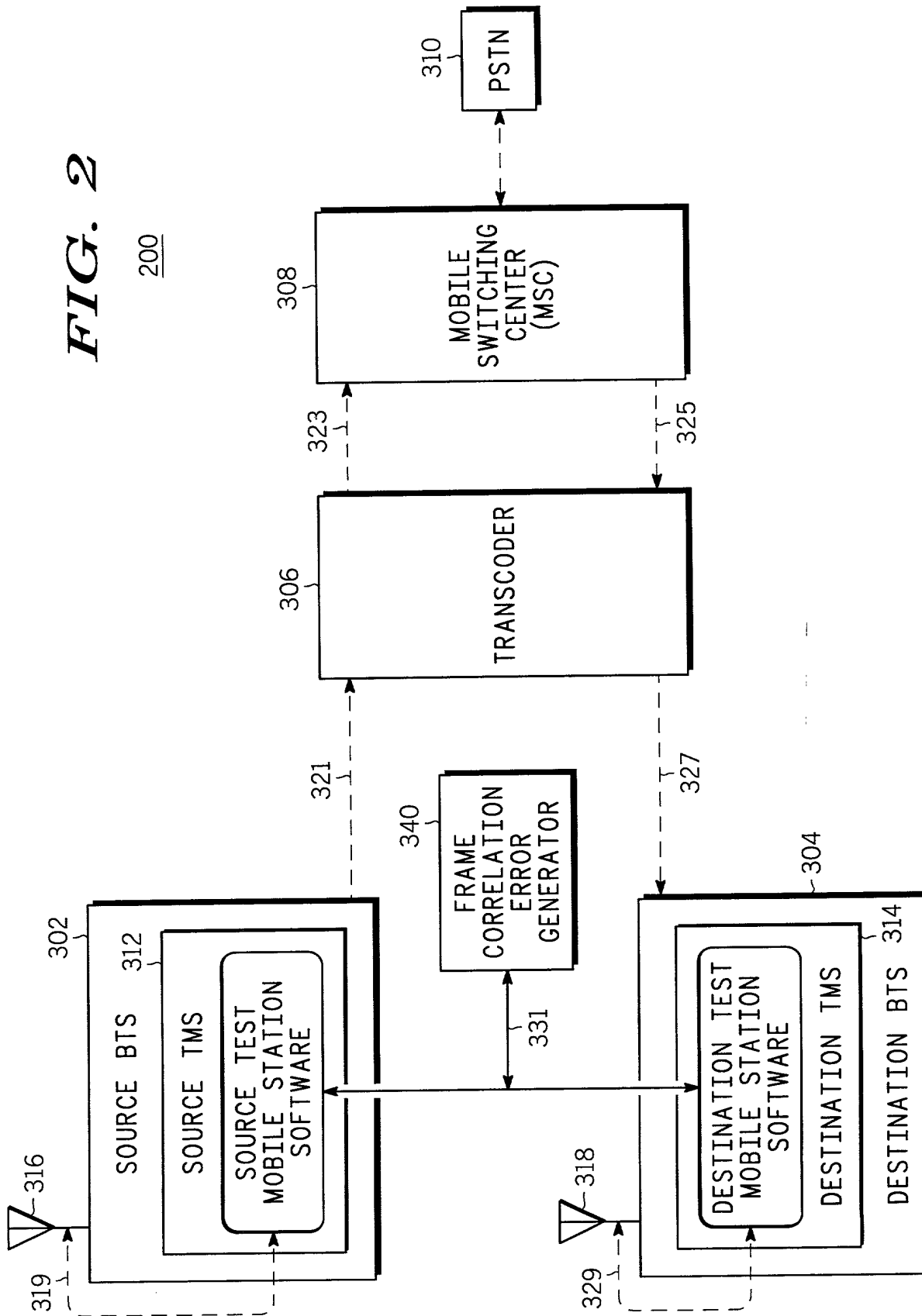


FIG. 3 is a block diagram of a system for testing digital channels in a wireless communication system. The system includes an LSP Frequency Extractor Module (502) which receives input signals 319 and 329. The output of module 502 is signal 501, which is fed into a Frame Aligner Module (504). The output of module 504 is signal 503, which is fed into a Frame Correlation Calculator (506). The output of module 506 is signal 507, which is fed into a Frame Correlation Selector Module (508). The output of module 508 is signal 511, which is fed into a Threshold Indicator (521). The output of module 521 is signal 513.

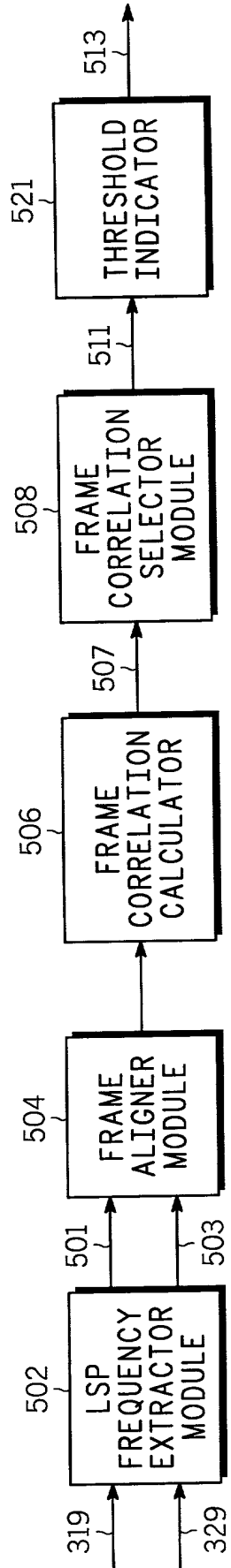
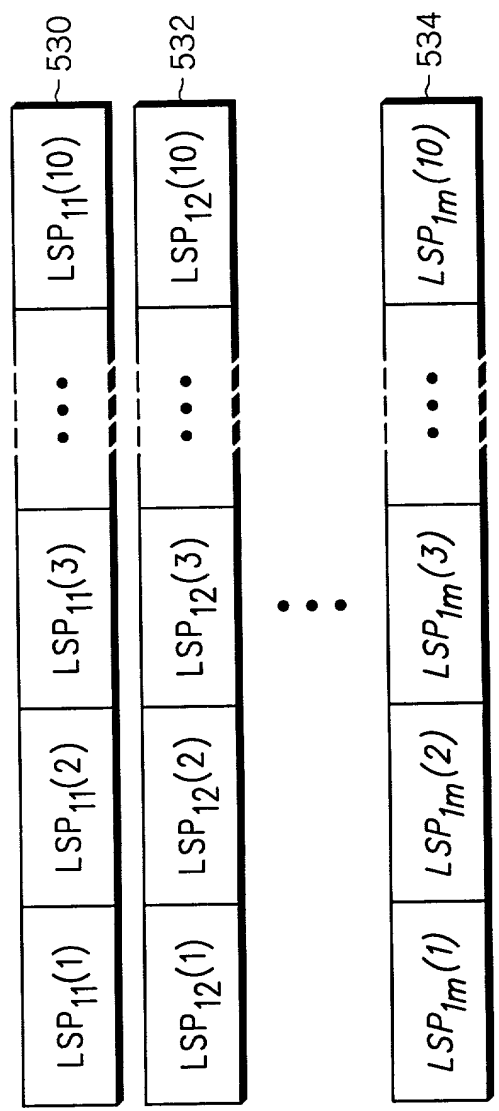


FIG. 3 340

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FIG. 4

501



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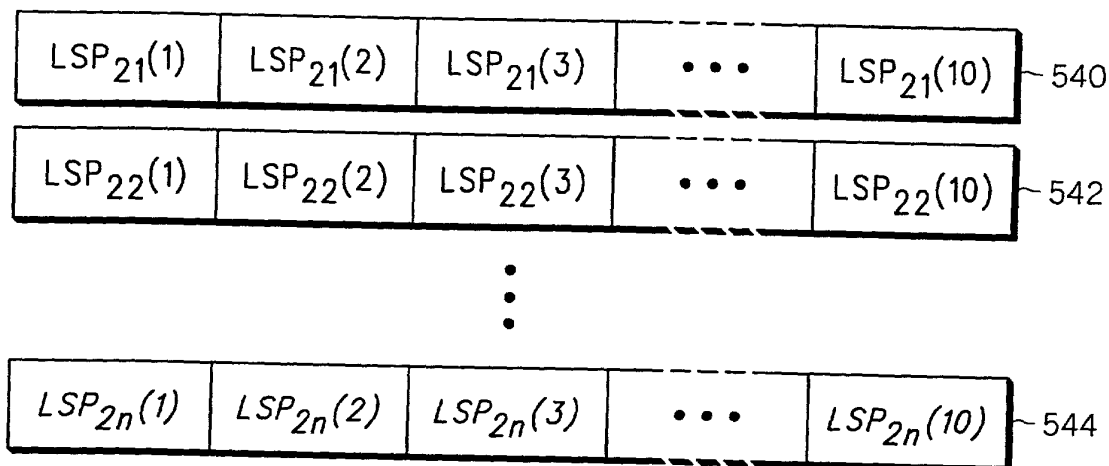
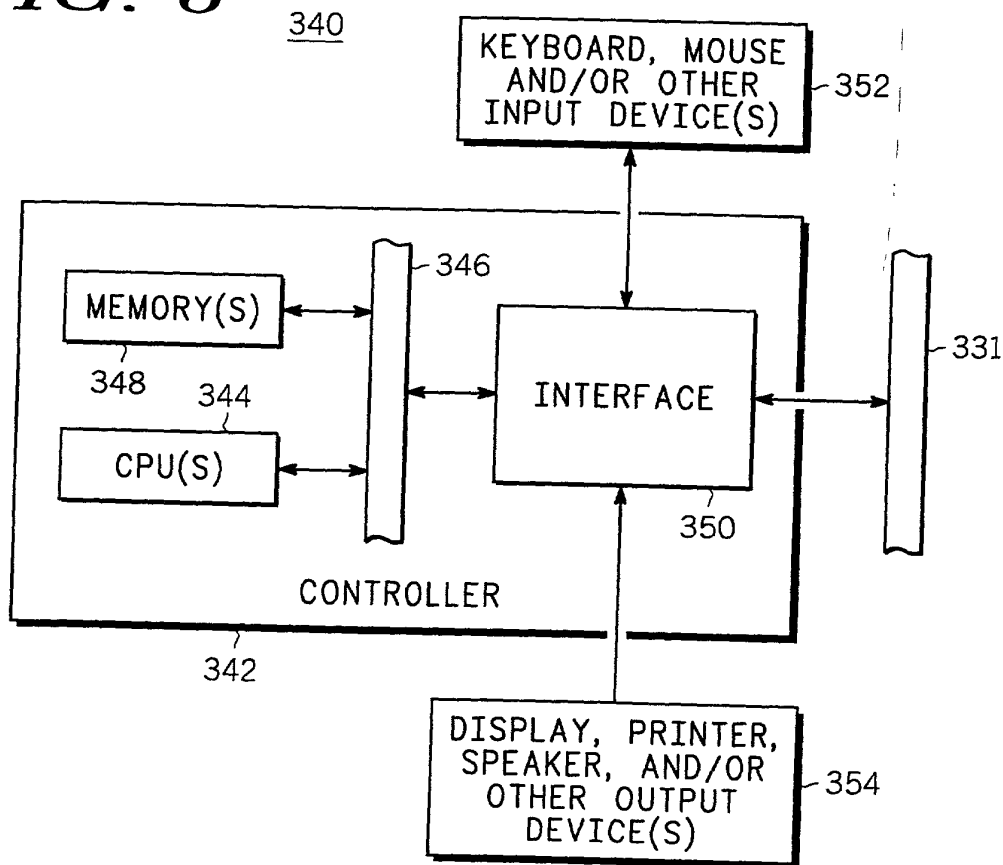


FIG. 5

503

FIG. 6



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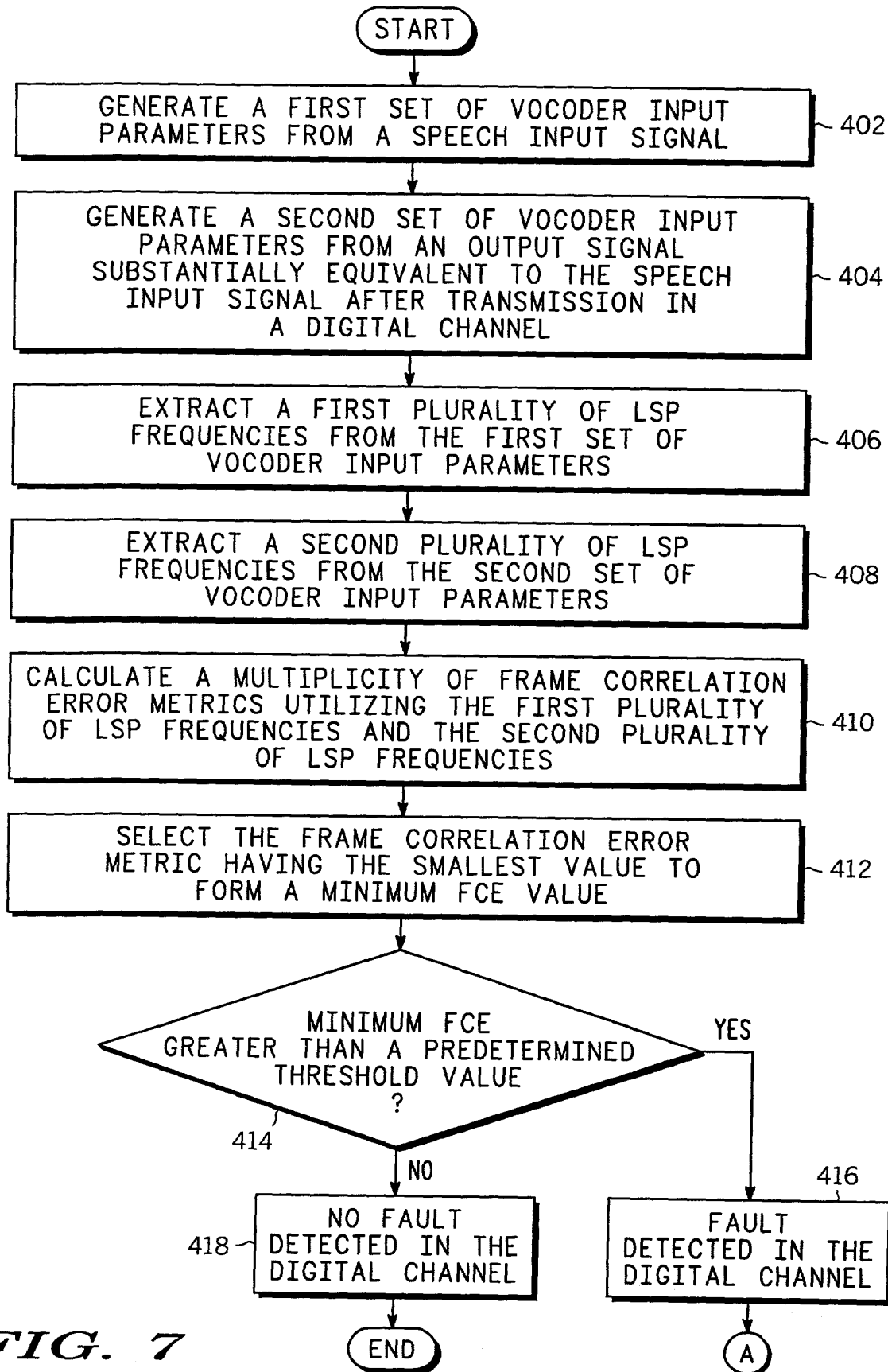


FIG. 7

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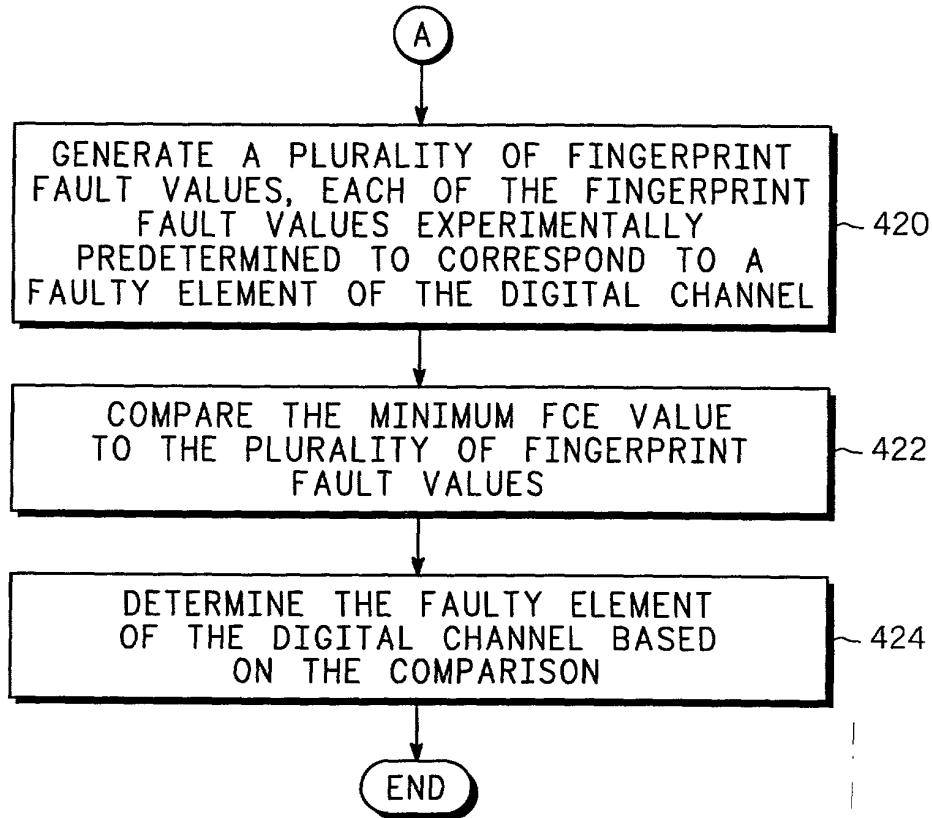


FIG. 8

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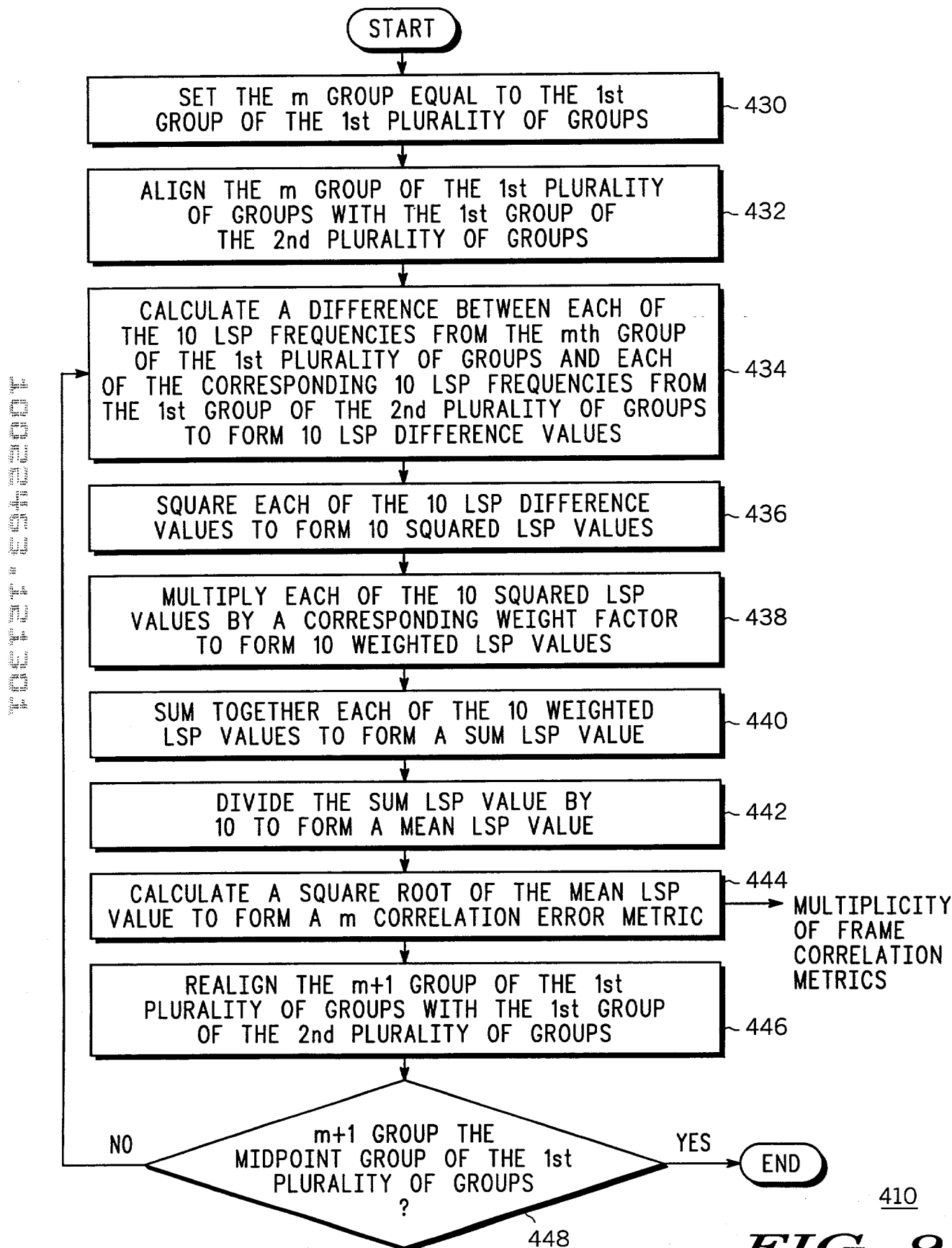


FIG. 9